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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/753,081

01/08/2004

Shinji Tanabe

OKI.633

5416

20987

7590

04/21/2005

VOLENTINE FRANCOS, & WHITT PLLC
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RESTON, VA 20190

EXAMINER

FENTY, JESSE A

ART UNIT

PAPER NUMBER

2815

DATE MAILED: 04/21/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

ALC

Office Action Summary

Application No.

10/753,081

Applicant(s)

TANABE ET AL.

Examiner

Jesse A. Fenty

Art Unit

2815

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 18 January 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-12, 18 and 19 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 18 and 19 is/are allowed.
- 6) ☒ Claim(s) 1, 4 and 8-12 is/are rejected.
- 7) ☒ Claim(s) 2, 3 and 5-7 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>11/18/05</u> | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claim 1 is rejected under 35 U.S.C. 102(e) as being anticipated by Hino et al. (U.S. Patent No. 6,873,053 B2).

In re claims 1, 4 and 8-12, Hino (esp. Fig. 6) discloses a semiconductor device,
comprising:

a semiconductor chip (21) which includes a circuit element (FET), a first wiring (57, over source) and an electrode pad (59), wherein the circuit element includes an electronic element, and wherein the first wiring is electrically connected between the circuit element to the electrode pad;

an insulating layer (55) formed on the semiconductor chip, wherein the insulating layer has an opening which is aligned over the electrode pad;

an external terminal (63) formed on the insulating layer;

a second wiring (61) formed on the insulating layer, wherein the second wiring electrically connects the electrode pad (59) to the external terminal (63) via the opening; and

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a shielding film (57, over field oxide 24) located in a same plane as the first wiring and interposed between the electronic element and the second wiring.

In re claim 4, Hino discloses the device of claim 1, wherein the first wiring is located on a multilevel interconnection structure having a plurality of interconnection layers, and wherein the shielding film is formed in the interconnection layers.

In re claim 8, Hino discloses the device of claim 1, wherein the first wiring and the shielding film are a same material.

In re claim 9, Hino discloses the device of claim 1. The limitation, "... includes an analog circuit ..." refers to the intended use of this device. Terms that simply set forth the intended use, a property inherent in or a function, do not differentiate the claimed composition of these elements from those known to prior art.

In re claim 10, Hino discloses the device of claim 1, further comprising a sealing resin (dielectric) which seals the second wiring, wherein a top surface of the external terminal is exposed from the sealing resin.

In re claim 11, Hino discloses the device of claim 1, wherein the external terminal includes a projection and an extended portion, wherein the extended portion extends from the second wiring to a top surface of the projection.

In re claim 12, Hino discloses the device of claim 10, further comprising a post electrode which connects the second wiring to the external terminal.

Allowable Subject Matter

3. Claims 2, 3 and 5-7 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

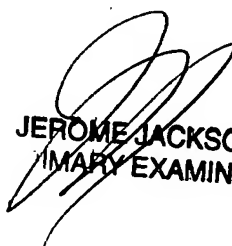
Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jesse A. Fenty whose telephone number is 571-272-1729. The examiner can normally be reached on 5/4-9 1st Fri. Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on 571-272-1664. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jesse A. Fenty
Examiner
Art Unit 2815


JEROME JACKSON
PRIMARY EXAMINER